

Electronic Acknowledgement Receipt

EFS ID:	4510361
Application Number:	09943512
International Application Number:	
Confirmation Number:	8183
Title of Invention:	Multilayer wiring board and semiconductor device
First Named Inventor/Applicant Name:	Yasuo Osone
Customer Number:	20457
Filer:	Gregory Edwards Montone/David Snyder
Filer Authorized By:	Gregory Edwards Montone
Attorney Docket Number:	500.40530X00
Receipt Date:	23-DEC-2008
Filing Date:	31-AUG-2001
Time Stamp:	11:38:58
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$130
RAM confirmation Number	8627
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)

1	Supplemental Response or Supplemental Amendment	40530X00SUPPLAMD.pdf	520622 66cdbe034f22451e55c399573c4f61189188 a6ea	no	15
Warnings:					
Information:					
2	Extension of Time	40530X00EOT.pdf	32933 9a84fdbdbe3212d890c999d49a120788888 37493	no	1
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	29891 4bac2d04fd8bed9d1110a0444612fec7aad cd60f	no	2
Warnings:					
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Total Files Size (in bytes):				583446	
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